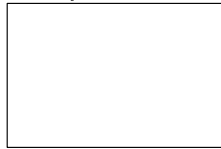


CPU



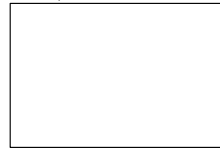
File: cpu.sch

CPLD Logic



File: cpld.sch

Memory



File: memory.sch

UART



File: uart.sch

Misc Logic



File: misc.sch

Ports & Slots



File: ports.sch

